

**IN THE DRAWINGS:**

Proposed corrected drawings are submitted herewith with changes marked in red in order to overcome the objections of the Official Draftsperson. It is noted that the unconnected lines in Figures 3 and 5 represent short and/or open circuits.



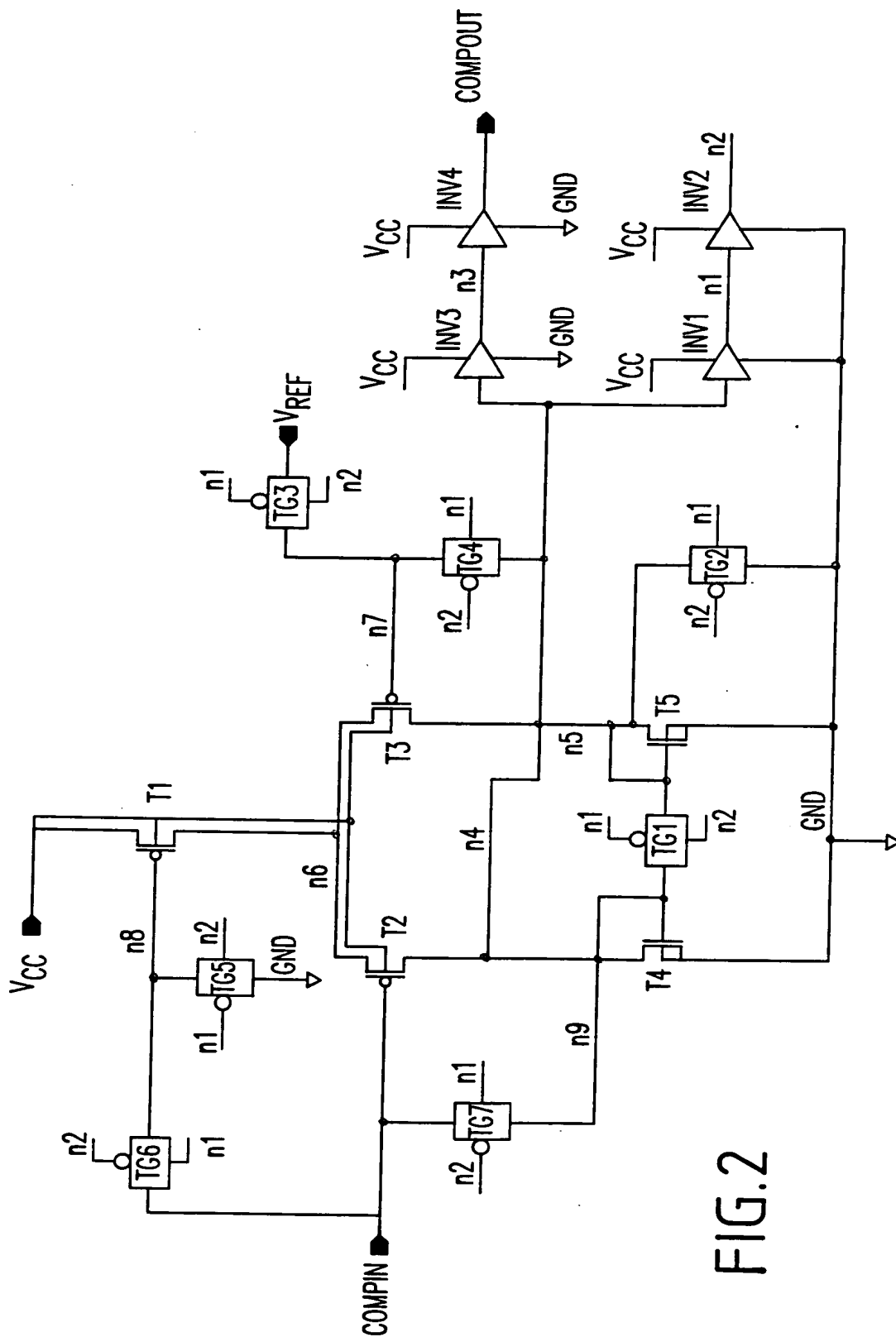


FIG. 2

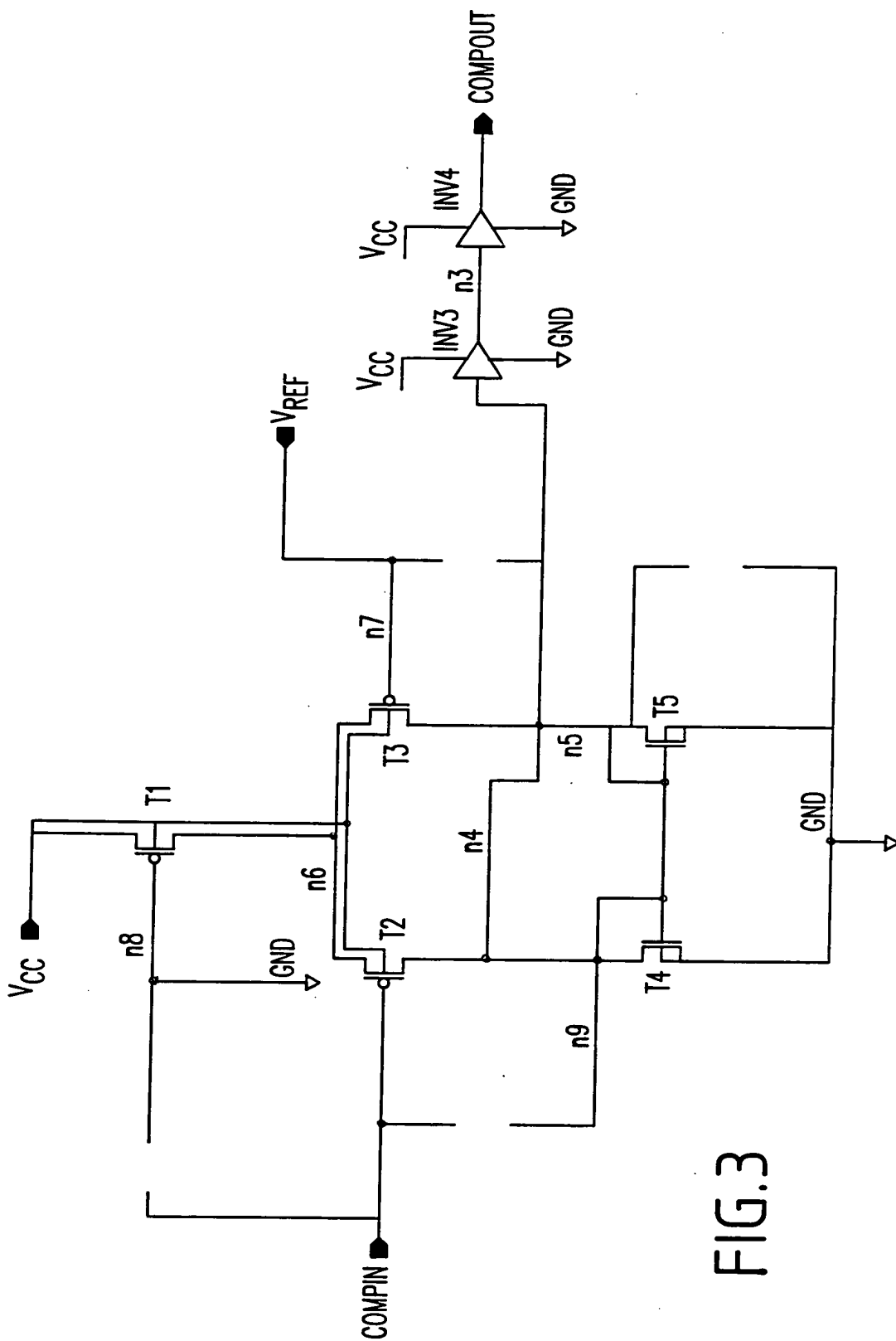
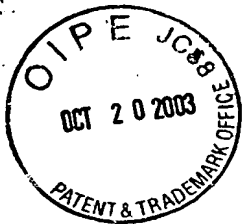


FIG. 3



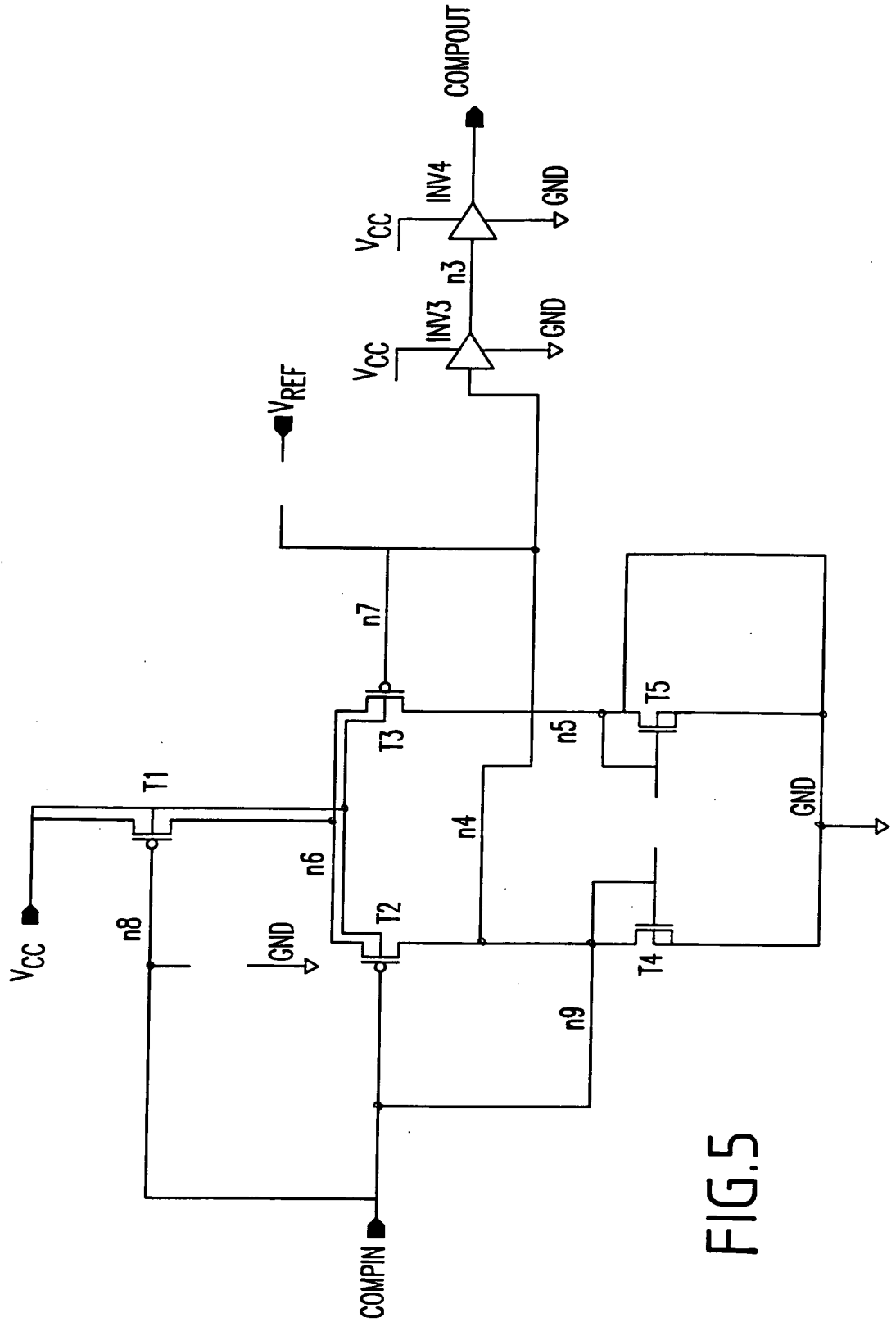
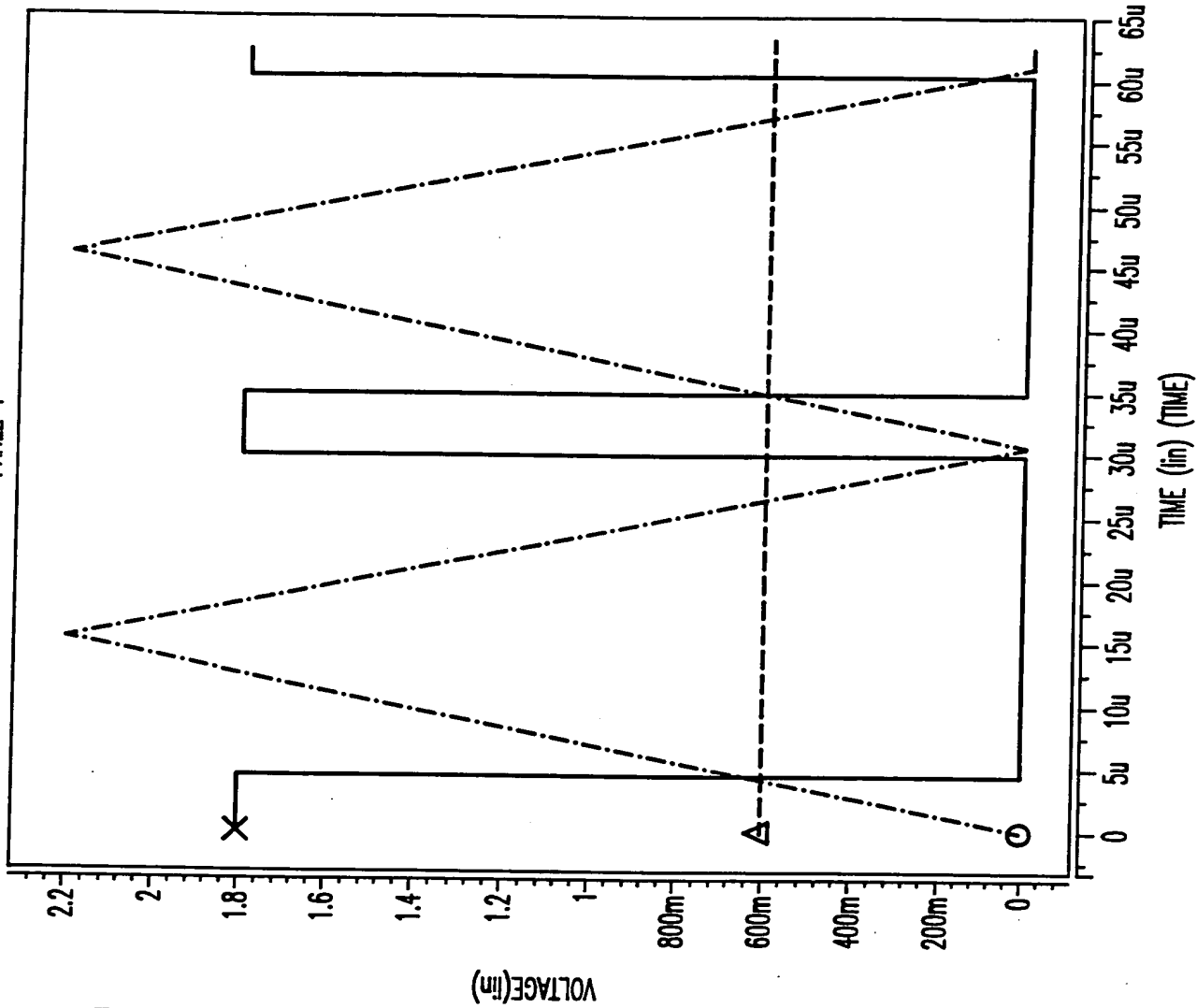


FIG.5





PANEL 1



WAVE	SYMBOL
DOADM (COMPIN)	○
DOADM (VREL)	△
DOADM (COMPUT)	X

FIG.7



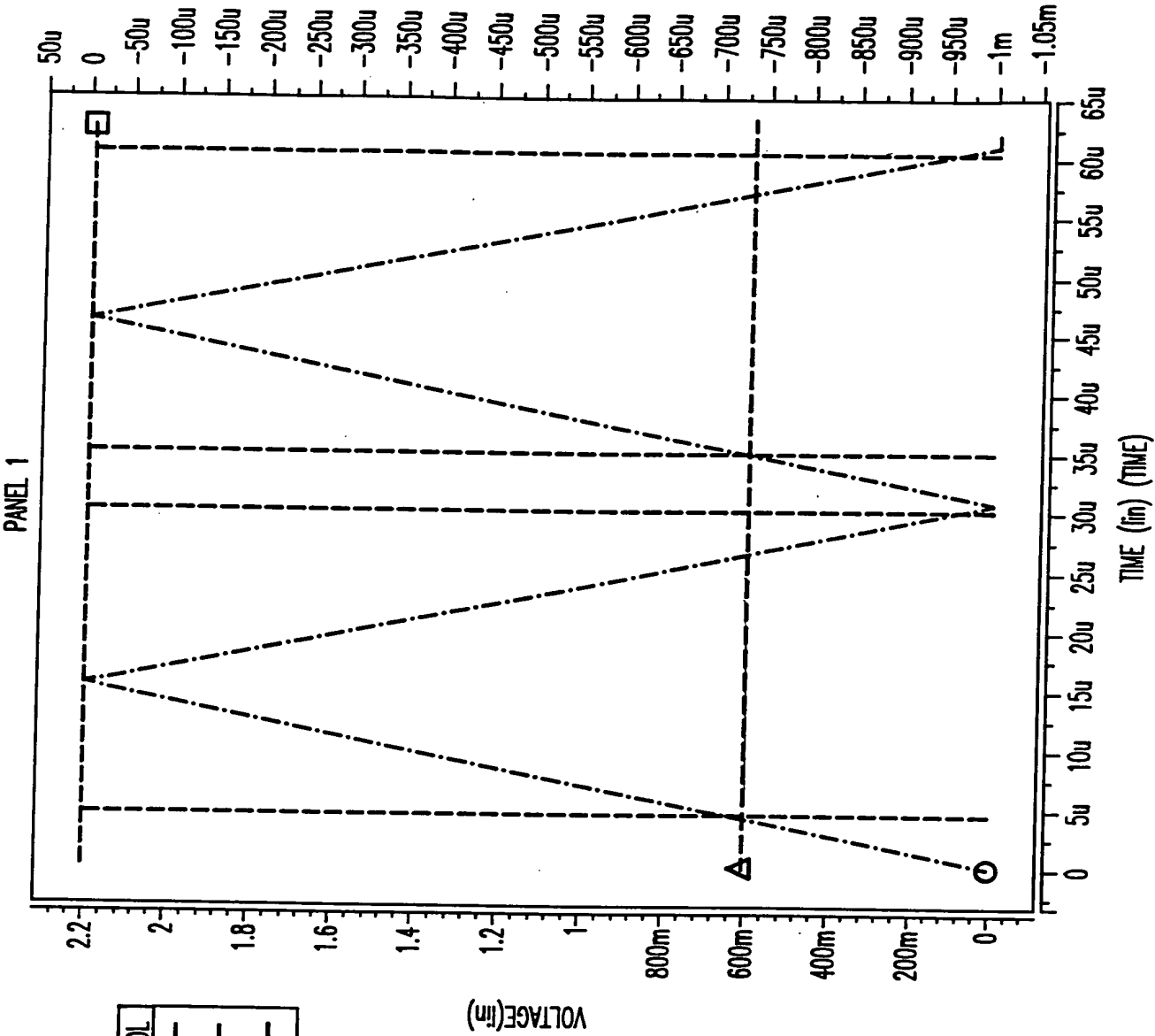


FIG. 8

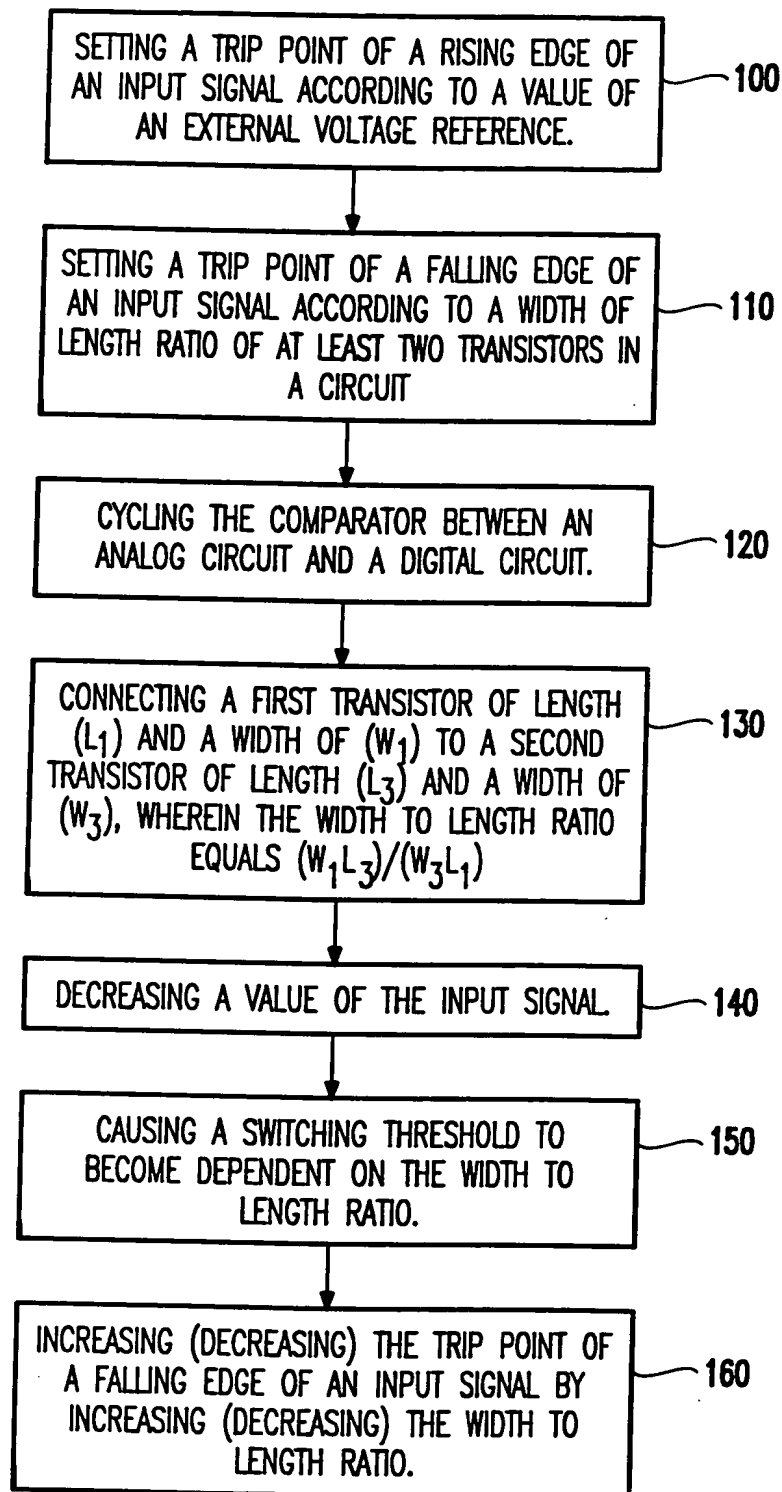
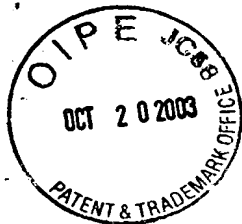


FIG.9